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APPLICANTS Andrew Strachan, Santa Clara, CA; Vladislav Vashchenko, Palo Alto, CA;				
** CONTINUING DATA ***** none				
** FOREIGN APPLICATIONS ***** none				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 12/02/2003				
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <i>Harold Daac</i> <i>SD</i> Examiner's Signature Initials		STATE OR COUNTRY CA	SHEETS DRAWING 3	TOTAL CLAIMS 3
INDEPENDENT CLAIMS 3				
ADDRESS STALLMAN & POLLOCK LLP ATTN: MICHAEL J. POLLOCK 353 SACRAMENTO STREET SUITE 2200 SAN FRANCISCO, CA94111				
TITLE Method of improving the breakdown voltage of a diffused semiconductor junction				
FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	